



SYN-0136

PATENT

#17/D  
12-18-03  
[Signature]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peter Wohl et al.

Assignee: Synopsys, Inc.

Title: METHOD AND SYSTEM FOR GENERATING AN ATPG MODEL OF  
A MEMORY FROM BEHAVIORAL DESCRIPTIONS

Serial No.: 09/360,069 File Date: July 23, 1999

Examiner: Eduardo Garcia-Otero Art Unit: 2123

Docket No.: SYN-0136 (RCE)

Date: November 25, 2003

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**

DEC 04 2003

Technology Center 2100

AMENDMENT IN RESPONSE TO ADVISORY ACTION,  
SUBMISSION OF REQUEST FOR CONTINUING EXAMINATION

INITIAL COMMENTS

Claims 37-58 are pending in the present application. Claims 1-36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Beausang (U.S. Patent 5,696,771) in view of MPEP 2144.04(II)(A) Routine Expedient Of Omission Of An Element And Its Function, Official Notice, and Cheng ("Gate-Level Test Generation For Sequential Circuits", ACM Transactions On Design Automation Of Electronic Systems, Vol. 1, No. 4, October 1996, pages 405-442).

Claims 1-36 are cancelled, thereby rendering the rejection of such claims moot. Claims 37-58 are added.